

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Currently Amended): A liquid crystal display device comprising:
- a first substrate with a pixel area;
  - ~~a first electrode pattern~~ a gate line on the first substrate, wherein ~~the first electrode pattern~~ the gate line includes an edge;
  - an insulating layer over ~~the first electrode pattern~~ the gate line;
  - ~~a second electrode pattern~~ a data line over the insulating layer, wherein ~~the second electrode pattern~~ the data line includes at least two conductive members;
  - a short-prevention member on the insulating layer, above the edge, and between the at least two conductive members; and
  - a pixel electrode in the pixel area;
- wherein the short-prevention member prevents electric shorts between the at least two conductive members caused by residual material that extends along the edge.
2. (Currently Amended): The liquid crystal display device of claim 1, wherein ~~the first electrode pattern~~ the gate line includes ~~a gate line~~, a gate electrode and a lower electrode of a storage capacitor.
3. (Currently Amended): The liquid crystal display device of claim 1, wherein ~~the second electrode pattern~~ the data line comprises ~~a data line~~, a source electrode, a drain electrode and an upper electrode of a storage capacitor.
4. (Original): The liquid crystal display device of claim 1, wherein the insulating layer forms a gate insulating layer.
5. (Original): The liquid crystal display device of claim 4, further comprising:
- a gate electrode under the gate insulating layer;
  - a semiconductor layer on the gate insulating layer and over the gate electrode; and

source and drain electrodes over the semiconductor layer.

6. (Currently Amended): The liquid crystal display device of claim [[1]] 5, wherein the short-prevention member is formed at a same time as the semiconductor layer.

7. (Currently Amended): The liquid crystal display device of claim [[1]] 5, wherein the short-prevention member is comprised of a same material as the semiconductor layer.

8. (Original): The liquid crystal display device of claim 1, further comprising: a lower electrode; and an upper electrode, wherein the lower electrode and the upper electrode are separated by the insulating layer.

9. (Original): The liquid crystal display device of claim 1, wherein the short-prevention member is formed as an island.

10. (Original): The liquid crystal display device of claim 1, further including:

a second substrate adjacent the first substrate; and

a liquid crystal between the first substrate and the second substrate.

11 -18 (Canceled).

19. (Currently Amended): A method of fabricating a liquid crystal display device, comprising:

forming ~~a first electrode pattern~~ a gate line on a first substrate having a pixel area;

forming an insulating layer over the first substrate and over ~~the first electrode pattern~~ the gate line;

forming a short-prevention member on the insulating layer and over an edge of ~~the first electrode pattern~~ the gate line;

forming ~~a second electrode pattern~~ a data line on the insulating layer; and

forming a pixel electrode in the pixel area;

wherein the short-prevention member is disposed to prevent electric shorts in ~~the second electrode pattern~~ the data line.

20. (Currently Amended): The method of claim 19, wherein ~~the first electrode pattern~~ the gate

line is formed using a wet etch process, and wherein ~~the first electrode pattern~~ the gate line includes ~~a gate line~~, a gate electrode, and a lower electrode of a storage capacitor

21. (Currently Amended): The method of claim 19, wherein ~~the second electrode pattern~~ the data line is formed using a wet etch process, and wherein ~~the second electrode pattern~~ the data line includes ~~a data line~~, source/drain electrodes, and an upper electrode of a storage capacitor.

22. (Original): The method of claim 20, further comprising:

forming a gate electrode under the insulating layer;

forming a semiconductor layer over the insulating layer; and

forming source/drain electrodes over the semiconductor layer.

23. (Original): The method of claim 22, wherein the short-prevention member is formed of a same material as the semiconductor layer.

24. (Original): The method of claim 19, wherein the short-prevention member is formed as an island.

25. (Original): The method of claim 19, wherein the short-prevention layer is formed by dry etching.

26 -33 (Canceled).

34. (New): The device of claim 2, wherein one of the short-prevention members is formed over an edge of the gate electrode.

35. (New): The method of claim 20, wherein one of the short-prevention members is formed over an edge of the gate electrode.

36. (New): A liquid crystal display device comprising :

a first substrate having a pixel area;

a gate electrode, a gate line and a first storage electrode on the first substrate, the gate electrode extending from the gate line and having an edge;

an insulating layer over the gate electrode, the gate line and the first storage electrode;

a semiconductor layer having a first width and partially overlapping the gate electrode at the edge;

a data line, source and drain electrodes and a second storage electrode over the insulating layer, the source electrode extending from the data line over the semiconductor layer, the drain electrode at least partially overlapping the semiconductor layer;

a short-prevention member on the insulating layer and extending from the semiconductor layer beyond the edge, the short-prevention member having a second width, the second width being smaller than the first width; and

a pixel electrode connected to the drain electrode;

wherein the short-prevention member extends sufficiently beyond the edge to prevent electric shorts between the source and drain electrodes caused by residual material remaining along the edge.

37. (New): A method of fabricating a liquid crystal display device, comprising:

forming a first substrate having a pixel area;

forming a gate electrode, a gate line and a first storage electrode on the first substrate, the gate electrode extending from the gate line and having an edge;

forming an insulating layer over the gate electrode, the gate line and the first storage electrode;

forming a semiconductor layer having a first width and partially overlapping the gate electrode at the edge;

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forming a data line, source and drain electrodes and a second storage electrode over the insulating layer, the source electrode extending from the data line over the semiconductor layer, the drain electrode at least partially overlapping the semiconductor layer;

forming a short-prevention member on the insulating layer and extending from the semiconductor layer beyond the edge, the short-prevention member having a second width, the second width being smaller than the first width; and

forming a pixel electrode connected to the drain electrode;

wherein the short-prevention member extends sufficiently beyond the edge to prevent electric shorts between the source and drain electrodes caused by residual material remaining along the edge.

38. (New): The liquid crystal display device of claim 1, further comprising a second short-prevention member.

39. (New): The liquid crystal display device of claim 19, further comprising a second short-term prevention member.

**Amendments to the Drawings:**

The attached sheets of drawings include changes to Figs. 1, 2, 3, 4, 5B and 5C. These sheets, which include Fig. 1-5C, replace the original sheets including Fig. 1-5C.

Attachment: Replacement Sheets  
Annotated Sheets Showing Changes